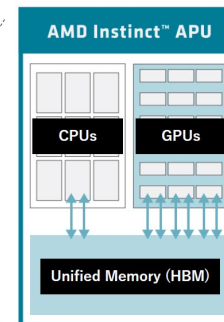
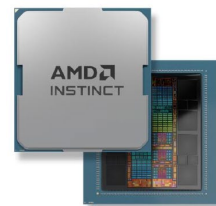


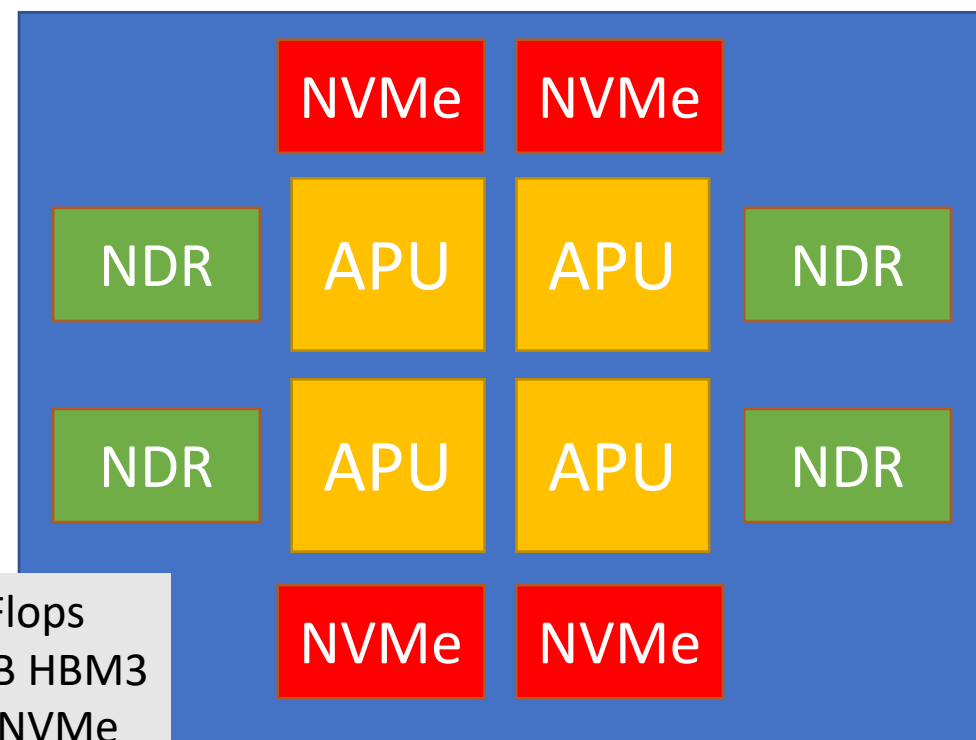


Sirius (PACS12.0) – Unified memory supercomputer

- Will be installed in Q1 2026
- Total Performance
 - 24 nodes, 11.905 PFlops, 12 TiB HBM3
- Node Specification
 - AMD Instinct **MI300A** APU x 4
(24c EPYC Zen 4 CPU, 122.6 TFlops CDNA 3 GPU, 128GB HBM3)
 - 3.84 TB PCIe Gen5 NVMe SSD x 4
 - InfiniBand NDR (400 Gbps) x 4
- Parallel File System
 - 5.2 PByte DDN EXAScaler (100 GB/s)
(DDN ES400NVX2 + SS9024 x 4)

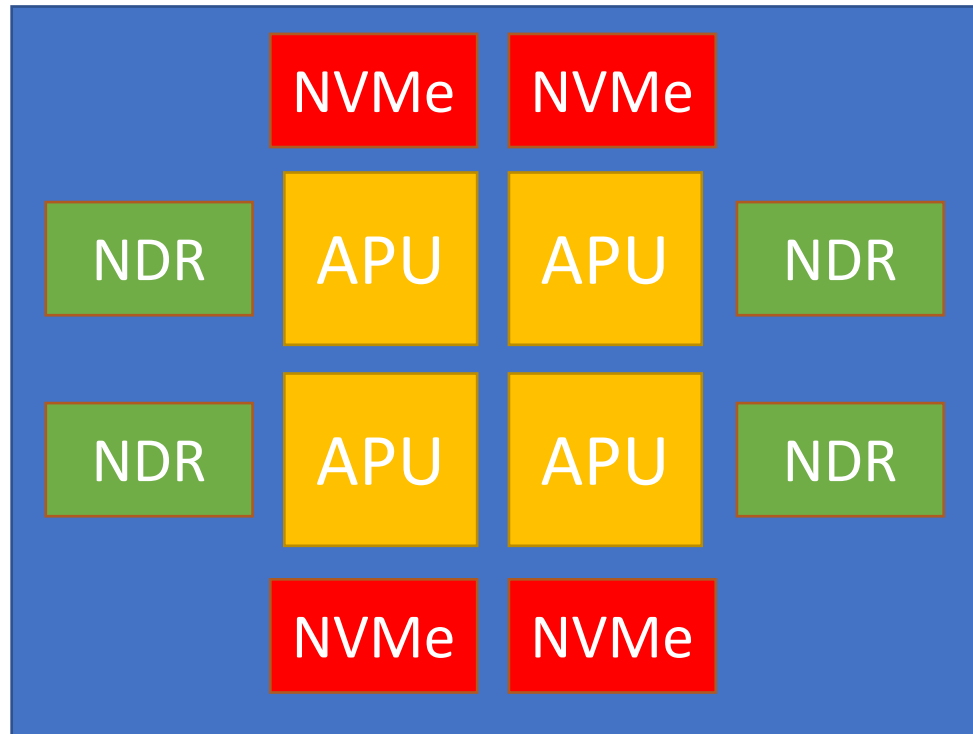


NEC LX 401Bax-3GA

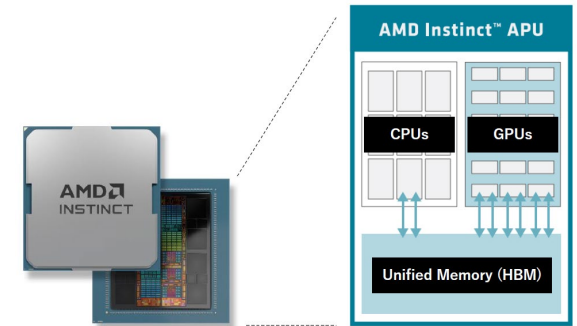


Sirius (PACS12.0) Unified Memory Supercomputer

400Gbps full bisection

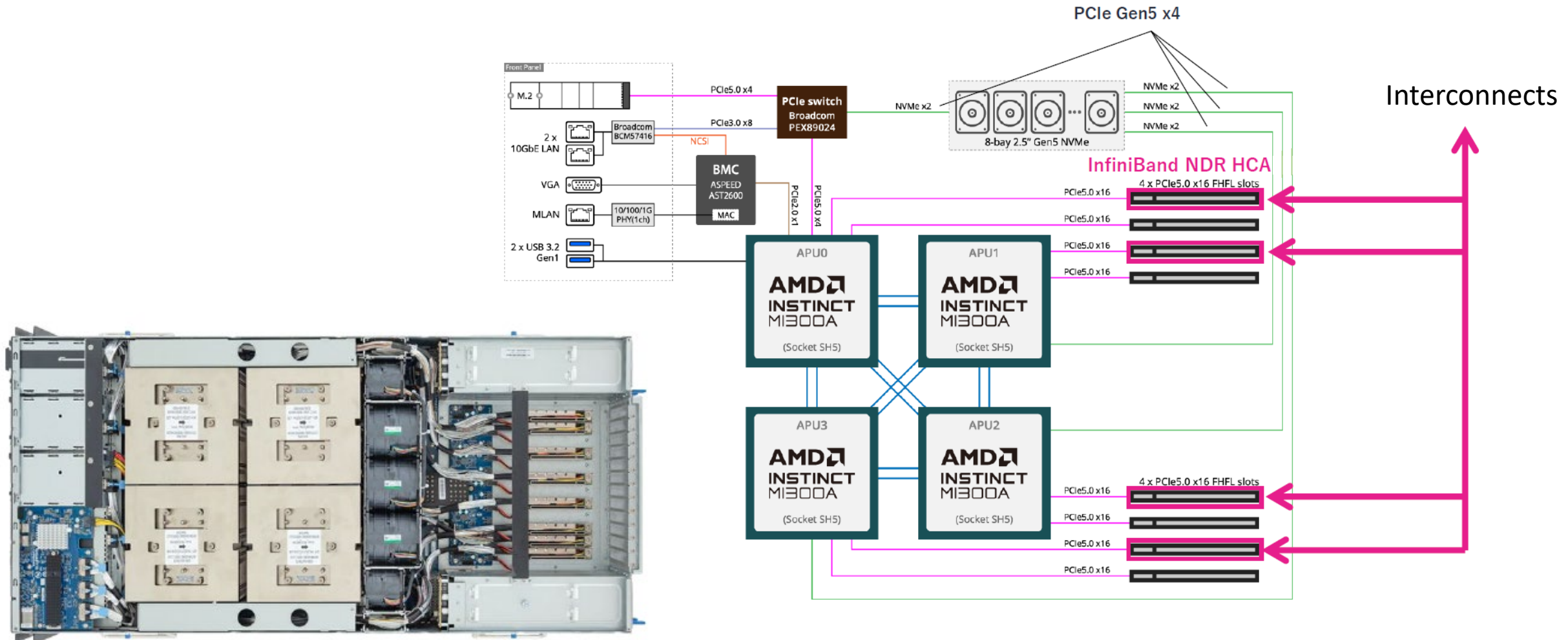


24 nodes



Parallel File System
5.2 PB, 100 GB/s,
22.5B inodes

Block diagram of compute node



NEC LX 401Bax-3GA