

# **3<sup>rd</sup> International Workshop on FPGA for HPC (IWFH)**

**March 12<sup>th</sup>, 2018**

**Akihabara, Tokyo, Japan**

# IWFH series

- **1st : Jan. 2016 at ANL**
- **2nd: Oct. 2016 at NCSA**
- **3rd: Mar. 2018 at Tokyo**
- **International Workshop on FPGA for HPC topic**
  - **highly technical forum to discuss FPGA (Field Programmable Gate Array) technology and research especially for HPC (High Performance Computing)**
  - **with many international invited talks with keynote for open discussion, and panel**
  - **not just by academia but also industry people are welcome**

# Agenda

10:00-10:15	<b>Opening: Welcome Remarks</b> Taisuke Boku (University of Tsukuba)
10:15-11:00	<b>Keynote: FPGA Datacenters for HyperScale HPC</b> <a href="#">Abstract</a> <a href="#">Andrew Putnam</a> (Microsoft)
11:00-11:30	<b>Data-Flow Hardware Optimization by Design Space Exploration</b> <a href="#">Abstract</a> Kentaro Sano (Riken)
11:30-11:45	<b>Break</b>
11:45-12:15	<b>Portable HPC with FPGAs should not be this hard: What to do about it</b> Martin Herbordt (Boston University)
12:15-12:45	<b>Case Studies: HPC Acceleration with Intel FPGAs</b> <a href="#">Abstract</a> Brad Kadet (Intel Programable Solutions Group)
12:45-14:15	<b>Lunch</b>
14:15-14:45	<b>Working toward performance portability for FPGAs in High Performance Computing</b> Jeff Vetter (ORNL)
14:45-15:15	<b>Key software challenges in FPGA-enabled heterogeneous platforms</b> <a href="#">Abstract</a> Kazutomo Yoshii (ANL)
15:15-15:45	<b>Developing applications with OmpSs@FPGA</b> <a href="#">Abstract</a> Xavier Martorell (BSC)
15:45-16:00	<b>Break</b>
16:00-16:30	<b>Computation/Communication offloading to FPGA with GPU</b> <a href="#">Abstract</a> Taisuke Boku (University of Tsukuba)
16:30-17:45	Panel: <b>Reconf500: Can we join the 500 club?</b> <a href="#">Abstract</a> Panelists: Andrew Putnam, Kentaro Sano, Jeff Vetter, Martin Herbordt, Naoya Murayama, Xavier Martorell (BSC) Moderator: Kazutomo Yoshii (ANL)
17:45-18:00	<b>Closing:</b> Taisuke Boku (University of Tsukuba)

# Organization & Foundation

- **by volunteer**
  - **Franck Cappello (ANL)**
  - **Taisuke Boku (U. Tsukuba)**
  - **Kaz Yoshii (ANL)**
- **3<sup>rd</sup> IWFH**
  - **supported by JST-CREST program “Research and Development on Unified Environment of Accelerated Computing and Interconnection for Post-Petascale Era” (PI: Taisuke Boku, 2013-2018)**
  - **cooperated by Center for Computational Sciences, University of Tsukuba**