

Programming Models and Tools: Programmer's Expectations

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Motivation

2



In the EU CoE project POP, and the German DFG project ProPE, we are developing a standardized performance engineering approach





https://pop-coe.eu/

Performance Audit -> Performance Plan -> Proof-of-Concept

In this talk, I will present my observations of what HPC users and HPC consultants expect from HPC programming models and tools

Agenda

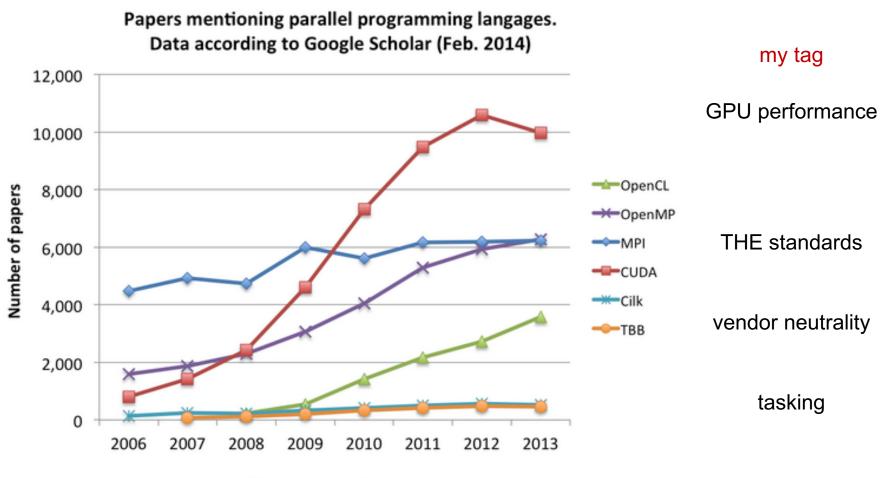


- Popularity of Programming Models
- POP Performance Engineering Process
- Requirements for Models and Tools
- Conclusion

Programming Models



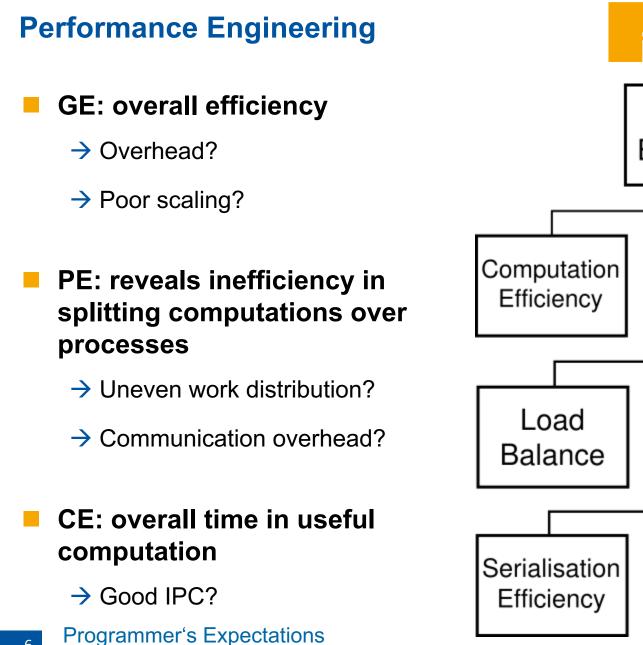
Popularity of Parallel Programming Models in terms of papers



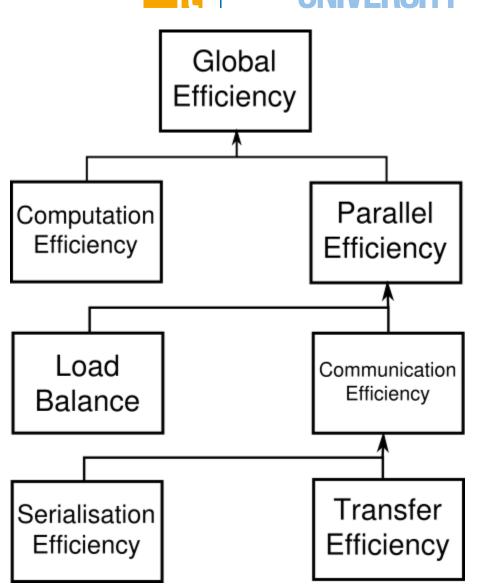
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POP Performance Engineering Process



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Optimization of RWTH IMM code GraGLe2D



- Performance issue: scalability of the OpenMP application over board boundaries on a big cc-NUMA-machine (BCS)
- Analyzed aspects: Performance Engineering Process
 - \rightarrow Thread binding \rightarrow bound manually, with OpenMP Affinity model
 - \rightarrow Load imbalance \rightarrow minor load imbalance, not serious
 - → Data placement → suboptimal due to a potential sharing of a single memory page by threads on two sockets/boards
 - \rightarrow Remote memory access \rightarrow a lot, due to suboptimal work distribution
 - \rightarrow Serial operations \rightarrow suboptimal, lots of unnecessary arithmetic operations

Optimizing strategies: Current Research: Standardization

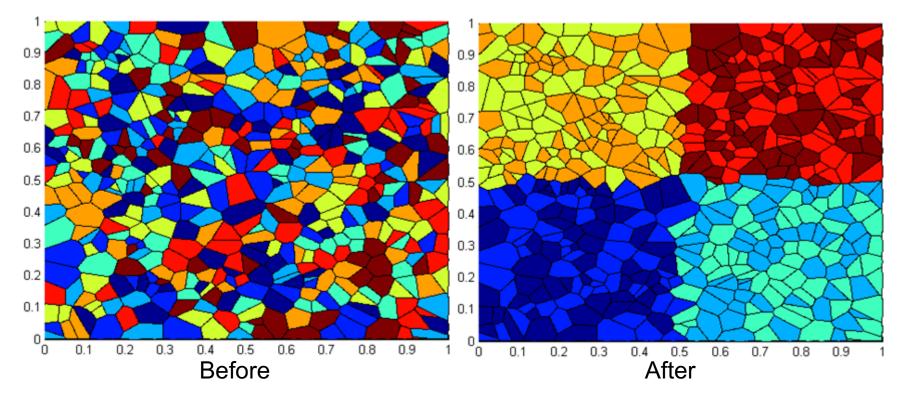
- → Using a scalable malloc routine instead of the system default malloc
- → Improve the load distribution: Adjacent loads processed by adjacent threads
- → Eliminate redundant/expensive operations, such as div/sqrt
- \rightarrow (Redesign the algorithm)
- → Vectorizing loops manually

Achieved performance (1)



Load distribution:

- → Work binding to threads, before and after optimization
- → Similar color: adjacent threads



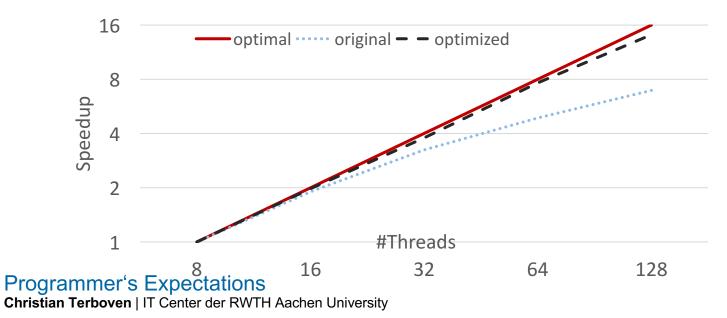
8



Runtime using 128 threads

	Application	Parallel Regions
Original run time(s)	341.49	212.41
Optimized run time(s)	154.68	33.15
Speedup	2.2	6.4

Scalability of parallel regions:





Requirements for Programming Models and Tools

Tool Support



Support for Performance Analysis

- → Following the PE process
- → Analyzing architecture-specific behavior
- → Differentiating between application and programming model

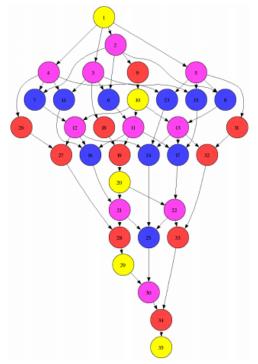
Insight into Innovative Features

- \rightarrow How to use them correctly
- → Example: Tasking

11

- →Granularity of Tasks?
- →Use of cut-off mechanism?

Support for Correctness Checking?



Feature Support



Programmers have a hard time coping with "new" memory types

- → Transactional Memory
 - →OpenMP's solution: annotated locks and critical regions
- → Locality / Memory Affinity
- → Non-volatile Memory
 - → OpenMP's solution: memory management API (in development)
 - → See TR5: www.openmp.org/...
- Abstractions improve productivity
- Standards + Standard Interface
 - → SPPEXA project MYX -> XMPT

→ Our group also contributes to OMPT (the OpenMP Tools Interface) Programmer's Expectations Christian Terboven | IT Center der RWTH Aachen University

Insight + Reproducibility + Reliability



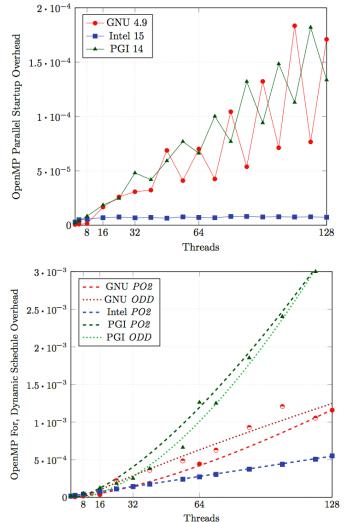
Features (abstractions) should work on a range of architectures

- Difference between theory and praxis: scaling of primitives
- → Bad example: overhead of OpenMP Parallel Region startup: high differences between implementations even on the same system
- Similar: overhead of dynamic
 loop scheduling

Do not break things with updates

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13





Conclusion

Programmer's Expectations



From yesterday's discussion: Application Analysis and Tuning is a never-ending process

- → Ongoing refinement of the Performance Engineering (PE) Process
- \rightarrow Opportunities for automatization?

High Quality tools needed for the PE process

 \rightarrow Opportunities for improvement

Lessons from applying the PE process

- → The number of programming models employed is increasing
- \rightarrow Programming Models have to be complemented with tools
- → Programming Models should be "reliable"

15



Thank for your attention.

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