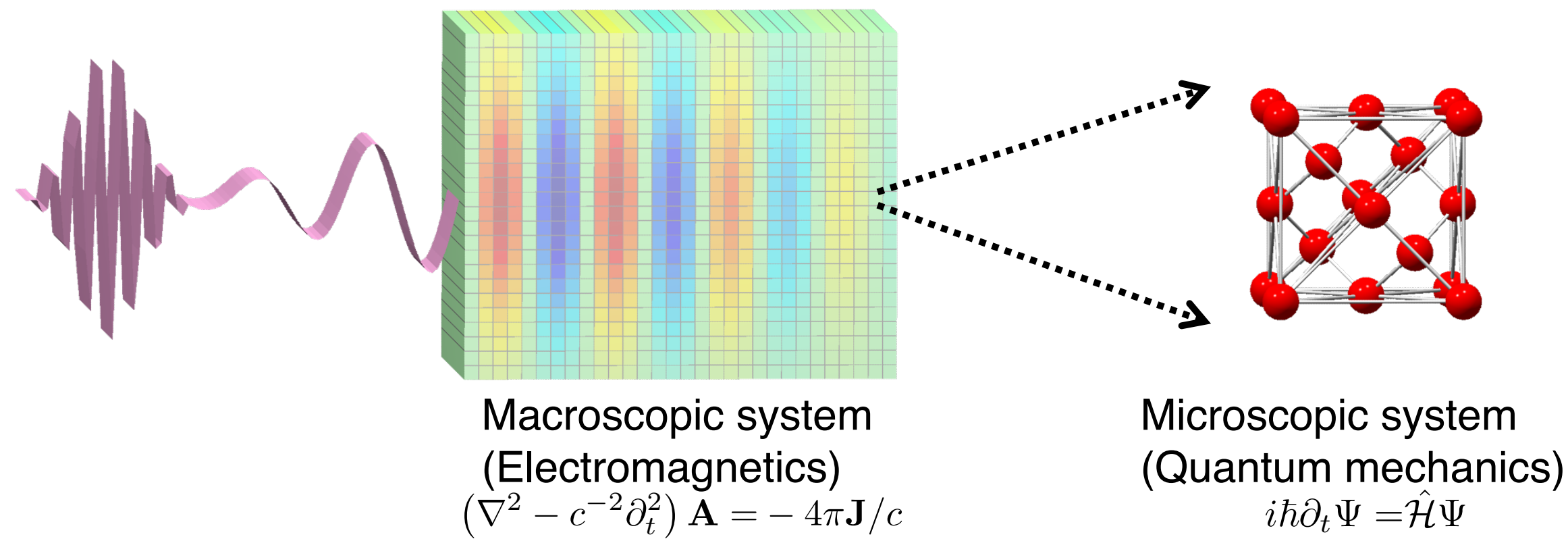


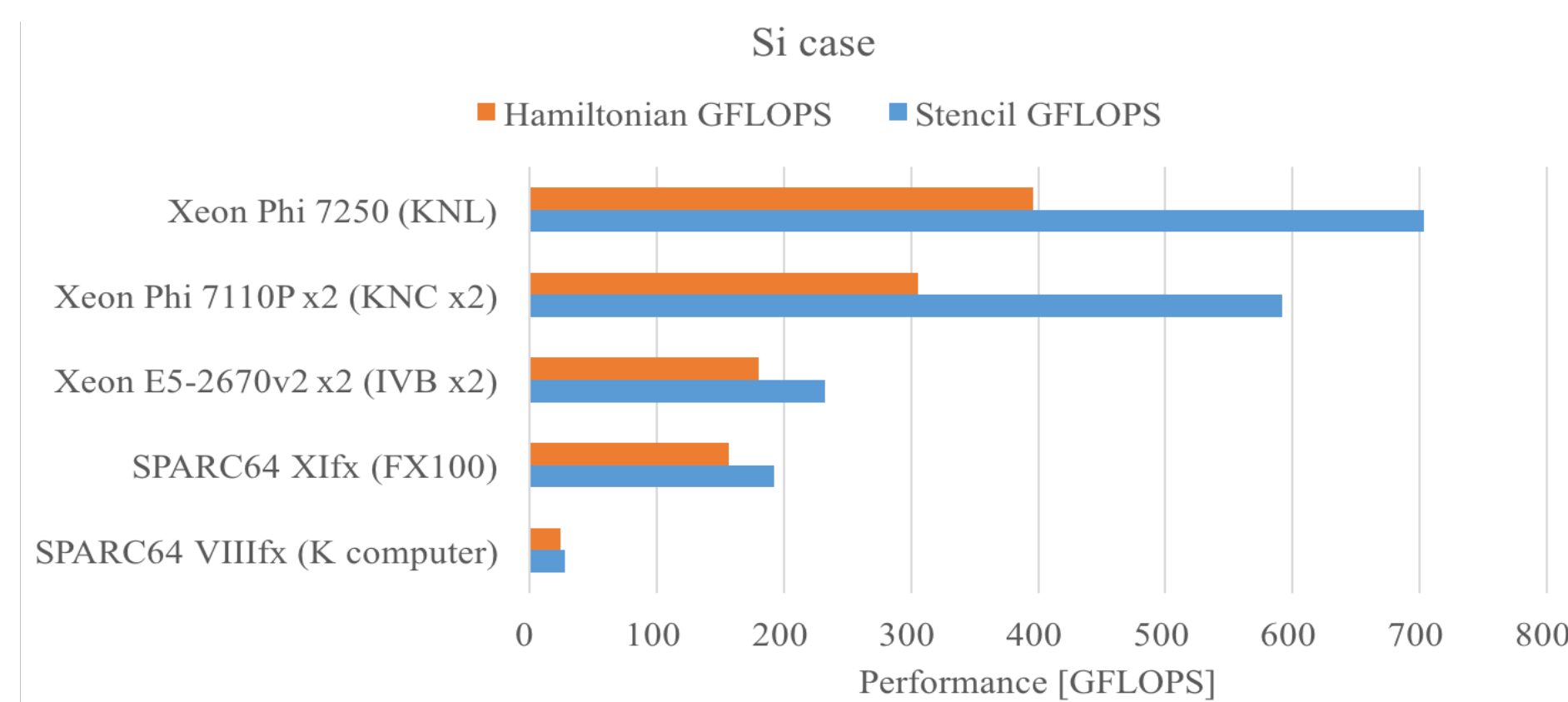
# Quantum Condensed Matter Physics

## ARTED: Ab-initio Real Time Electron Dynamics simulator

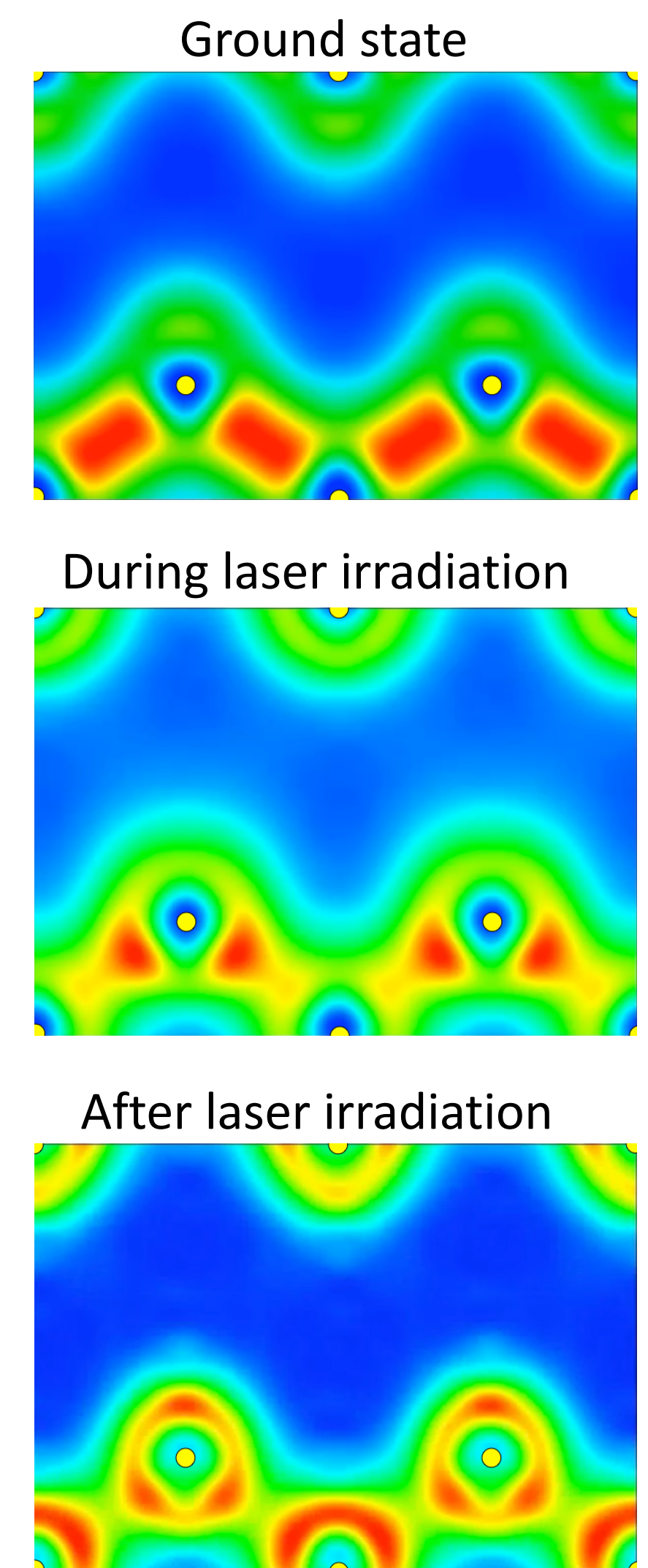
Interactions between light and matter are the basis of a wide range of technologies. To gain a basic understanding of the interaction, it is essential to grasp electron dynamics in matter induced by the light electromagnetic fields in a microscopic scale,  $10^{-9}$  (nano-)meter in space and  $10^{-15}$  (femto-) second in time. We have been developing a computer code ARTED, Ab-initio Real Time Electron Dynamics simulator. It is based on time-dependent density functional theory in microscopic scale. ARTED is available at <https://www.github.com/ARTED/ARTED>.



1. ARTED solves coupled dynamics of electrons and electromagnetic fields in a multiscale modeling. In microscopic scale, time-dependent Kohn-Sham equation is solved in real space grid representation. Maxwell equations for the light electromagnetic fields are solved employing finite-difference time-domain method.



3. ARTED is optimized for latest processors of different architectures. Performances of ARTED (Hamiltonian and stencil operations) are compared.



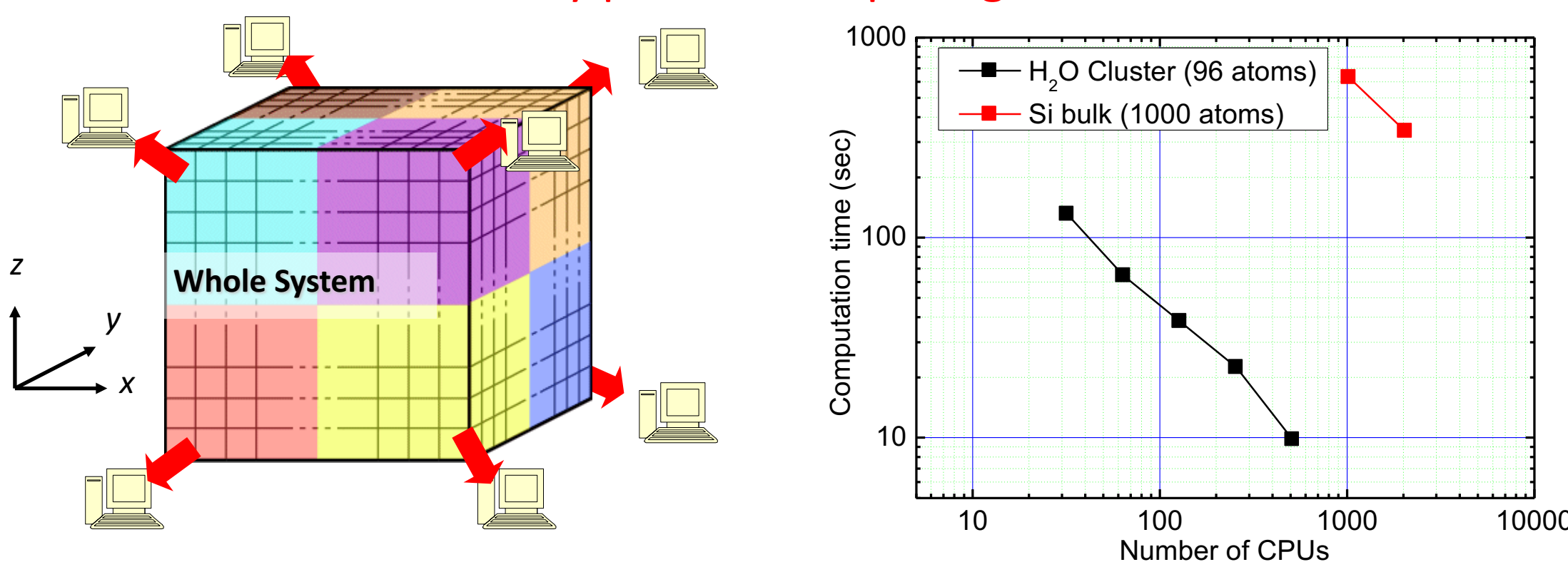
2. Electron motion in crystalline silicon induced by intense and ultrashort laser pulse.

## Computational Design of Future Electronics Devices using First-Principles Calculations

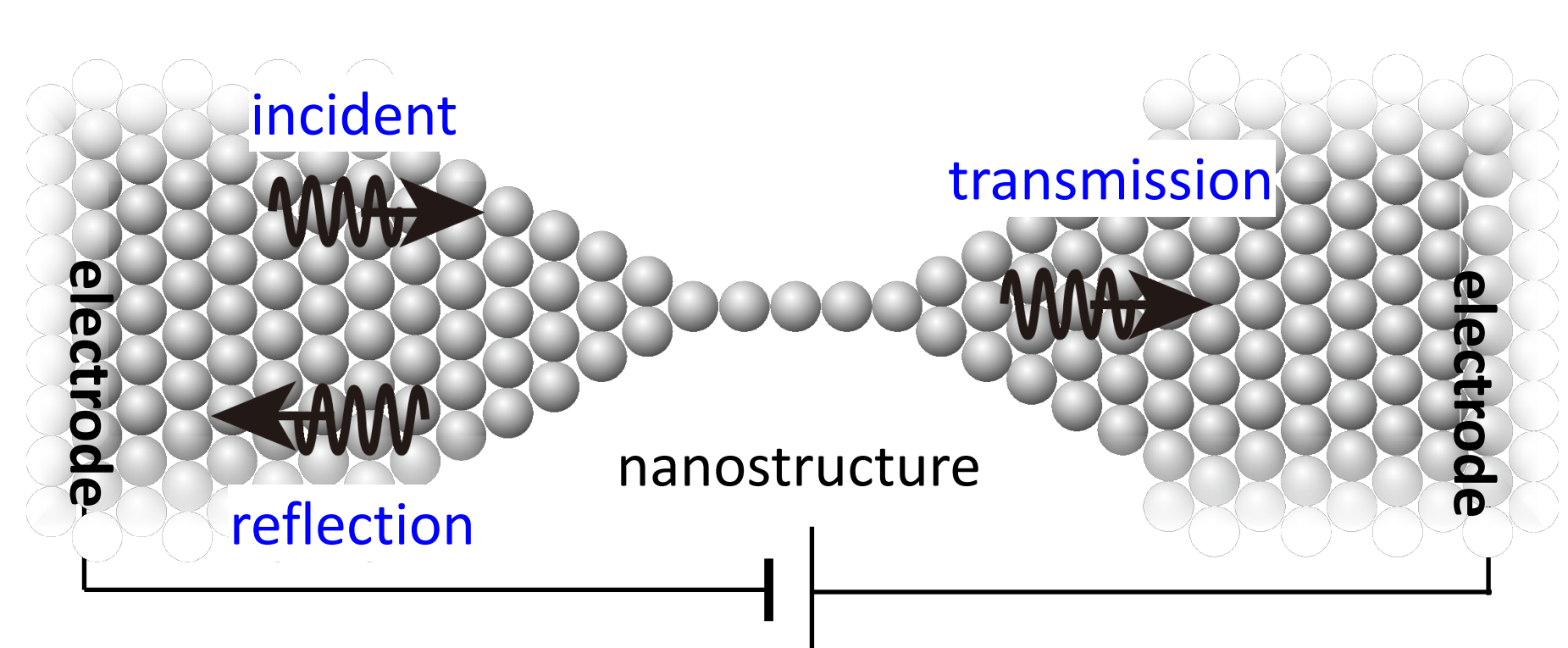
With cutting-edge materials and minute electronic devices being produced by the latest nanoscale fabrication technology, it is essential for scientists and engineers to rely on first-principles calculations to fully understand phenomena from the behavior of electrons. We develop first-principles calculation code "**RESPACE**."

**RESPACE** uses real-space finite-difference method for first-principles calculation. The advantages of **RESPACE**, which are superior to conventional plane wave methods, are

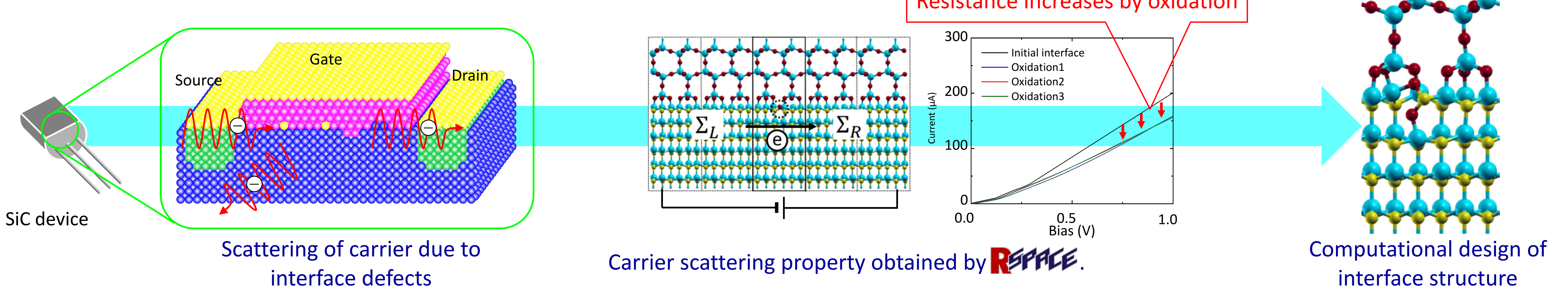
1. Suitable for massively parallel computing.



2. Applicable to transport calculations.



As applications using **RESPACE**, interface atomic structures are examined and fabrication procedures are developed to realize high-performance and low-energy-loss electronic devices, e.g., SiC power devices. By investigating the origin of leakage current and carrier scattering at interfaces, we propose prescriptions to improve device performance and demonstrate the applicability of the interface structures.



Furthermore, we establish a basic technology of computational science to design interface structures and fabrication procedures of future electronic devices.