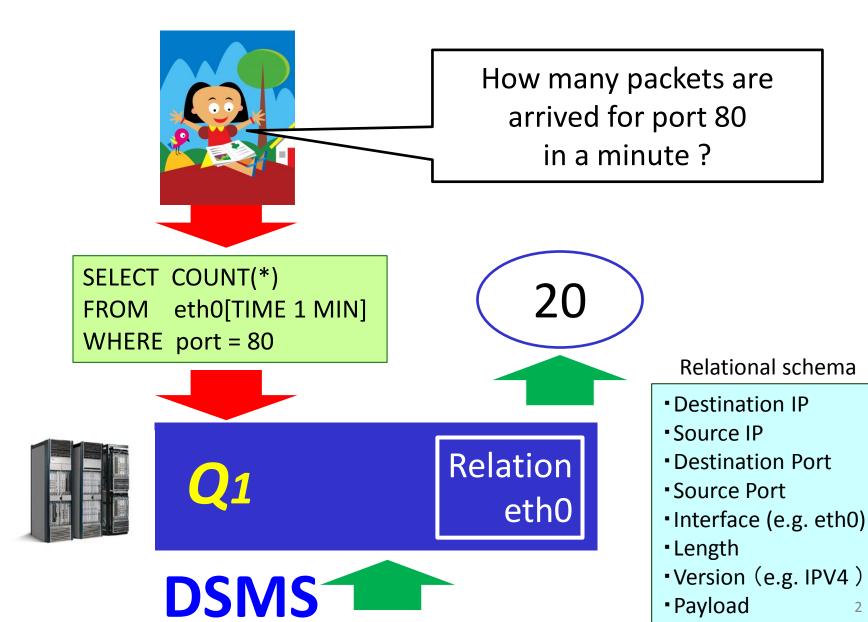
A Fast Window Join over Data Streams

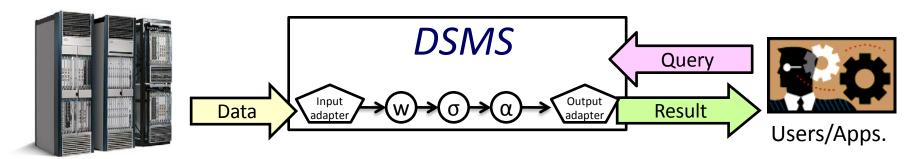
Hideyuki Kawashima

Presented at ICNC'11, FPL'11, MCSoC'12, SSDBM'13.

Data Stream Management System (DSMS)



SELECT COUNT(*)
FROM eth0[TIME 1 MIN]
WHERE port = 80



- SQL is translated to operator tree.
- On arrival of data, tree is evaluated.
- Operators are based on relational database
 - w(Window): Cutting off relations from a stream
 - $-\sigma$ (Selection): Filter
 - $-\alpha$ (Aggregation): such as AVG, MIN, MAX

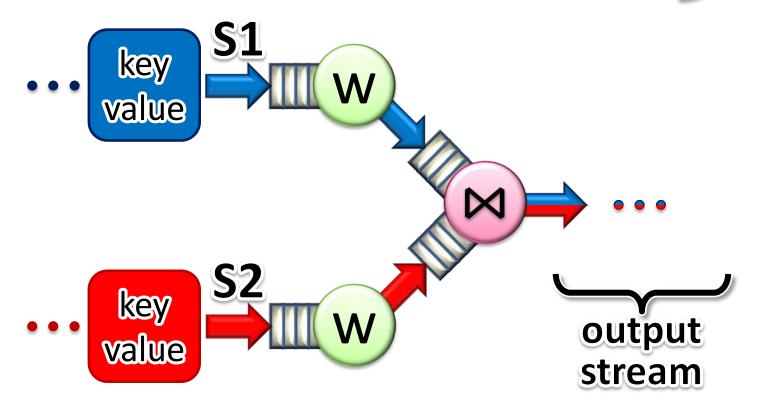
Window Join

SELECT *

FROM S1 [Rows 100], S2 [Rows 100]

WHERE S1.key = S2.key

CQL
Query





HANDSHAKE JOIN

J. Teubner and R. Müller, SIGMOD'11

Handshake Join

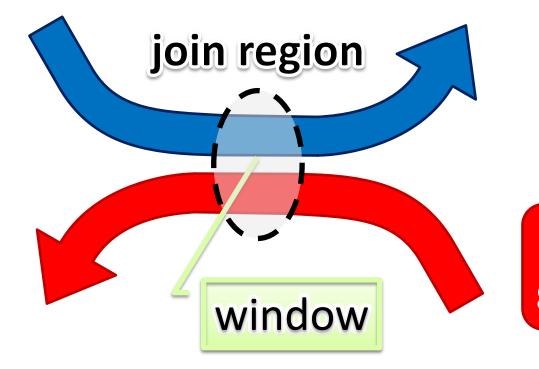
basic idea

Flow in opposite direction

advantage

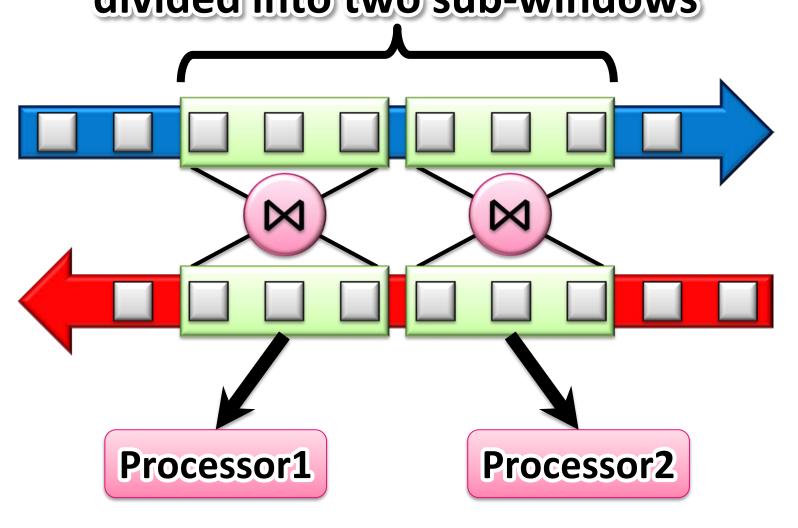
Highly parallel evaluation

input stream 1

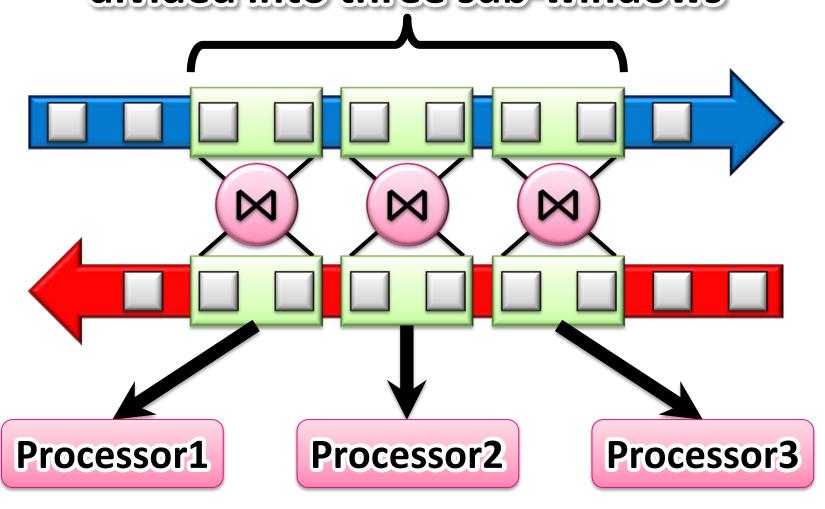


input stream 2

Handshake Join | Parallelization divided into two sub-windows



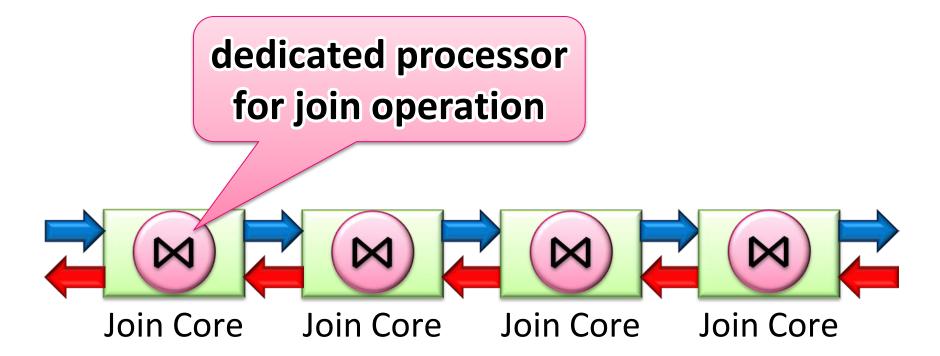
Handshake Join | Parallelization divided into three sub-windows



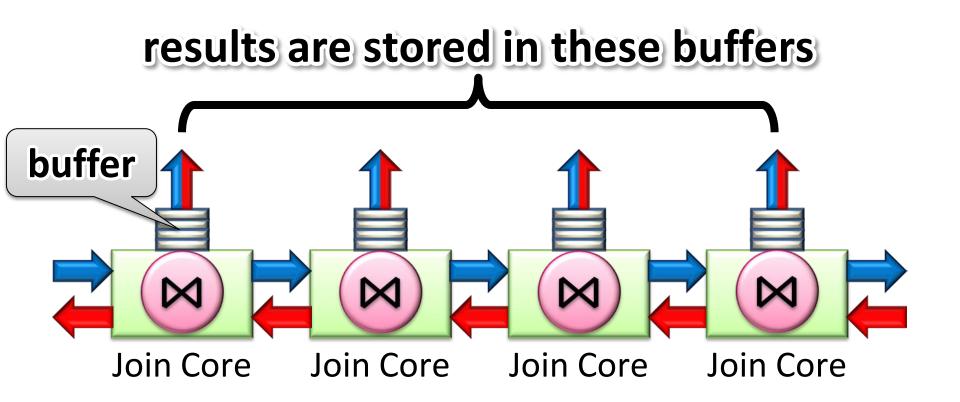


Naïve IMPLEMENTATION

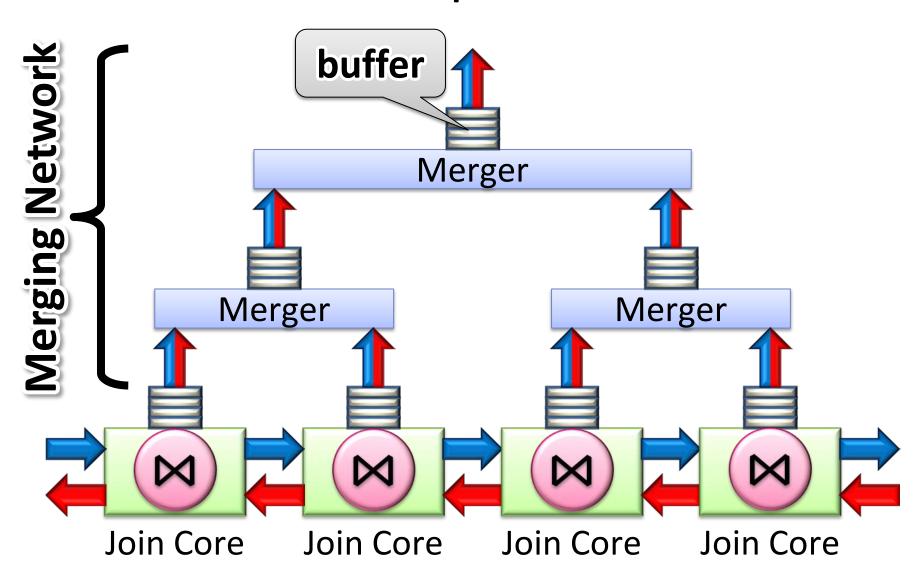
Baseline Implementation



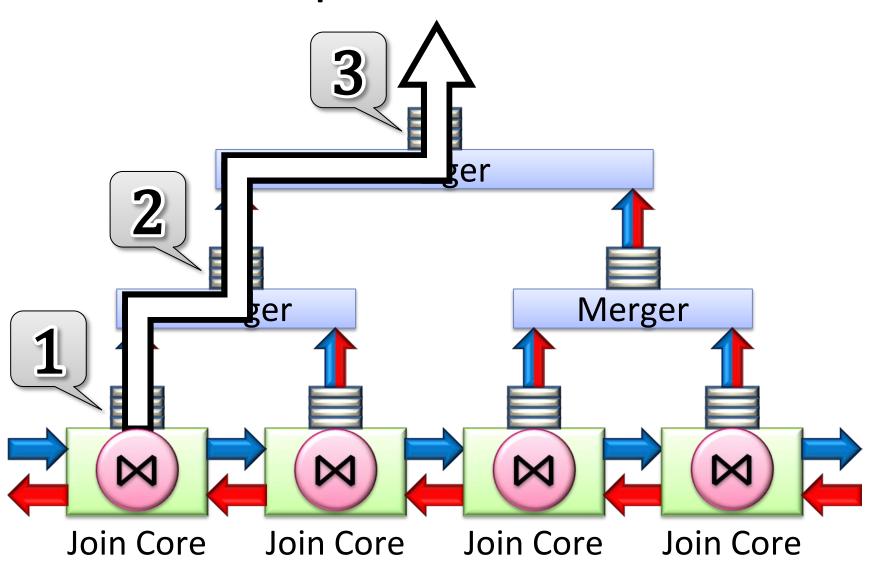
Baseline Implementation

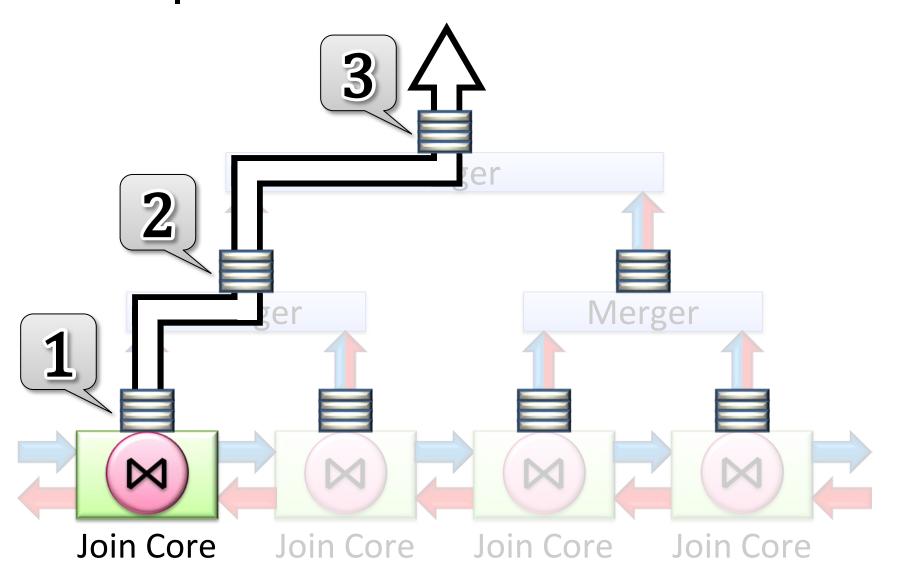


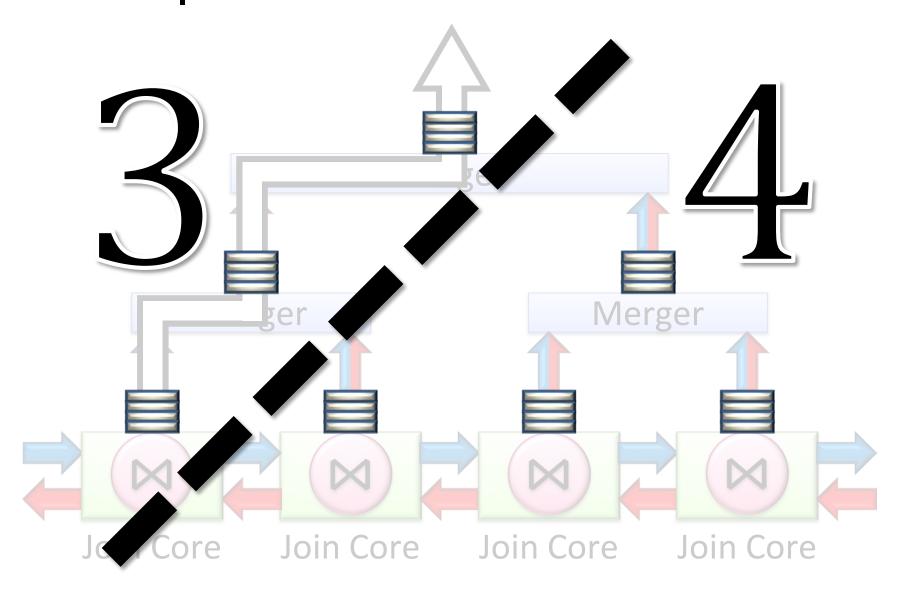
Baseline Implementation

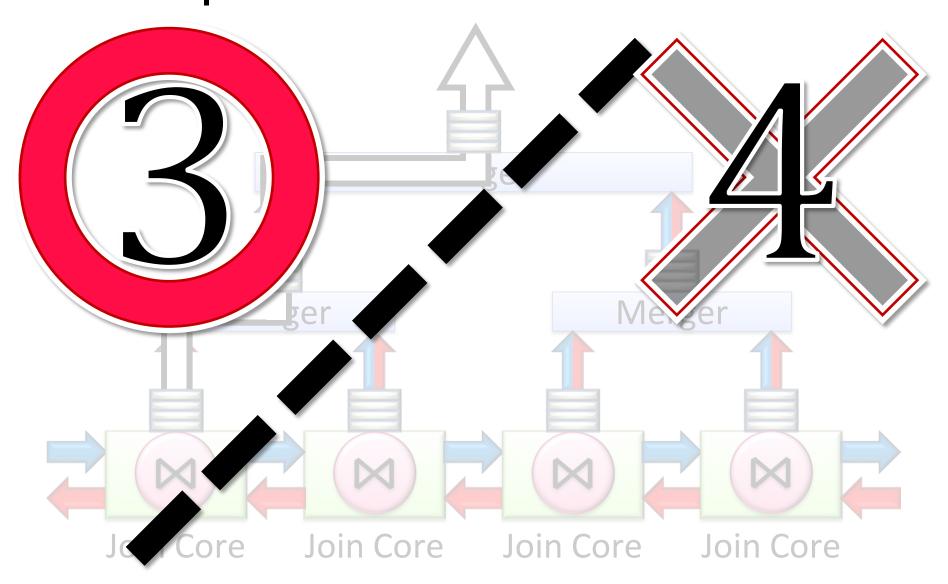


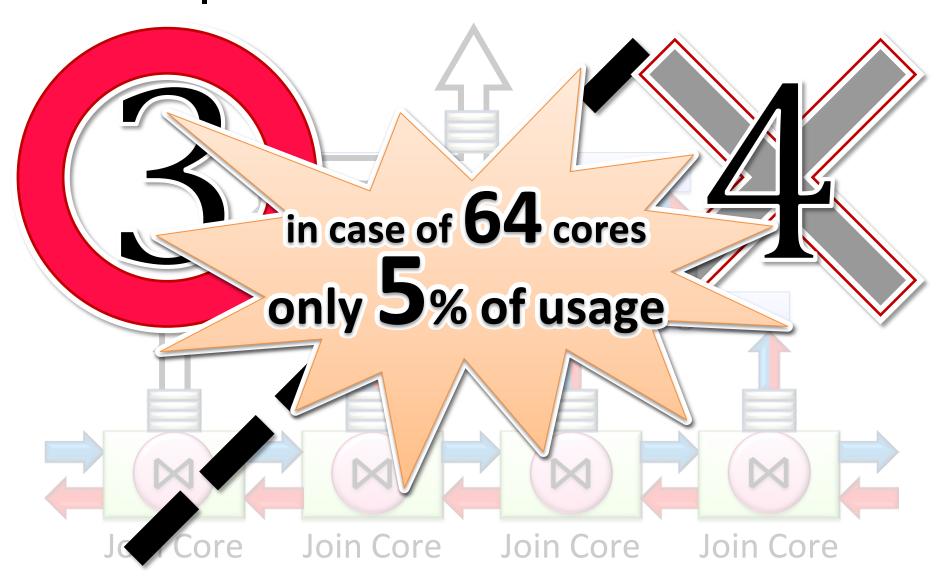
Output Data Flow





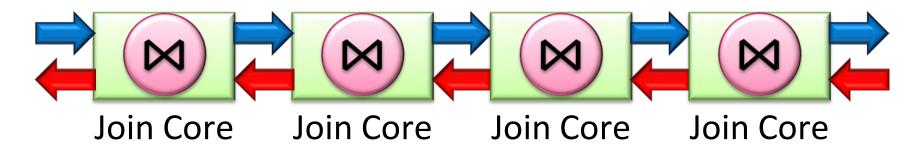


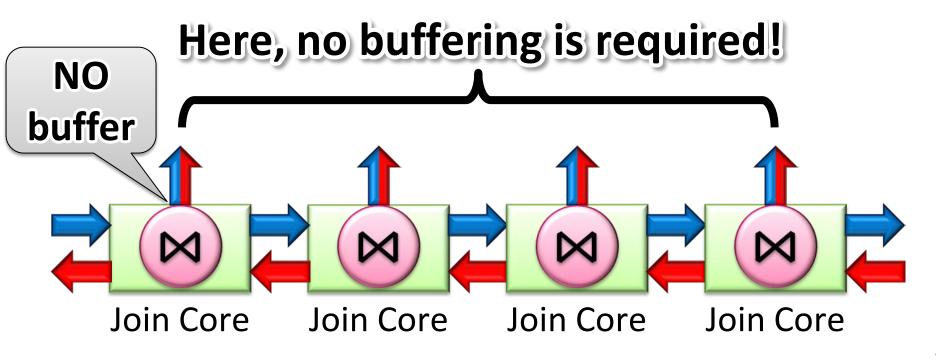


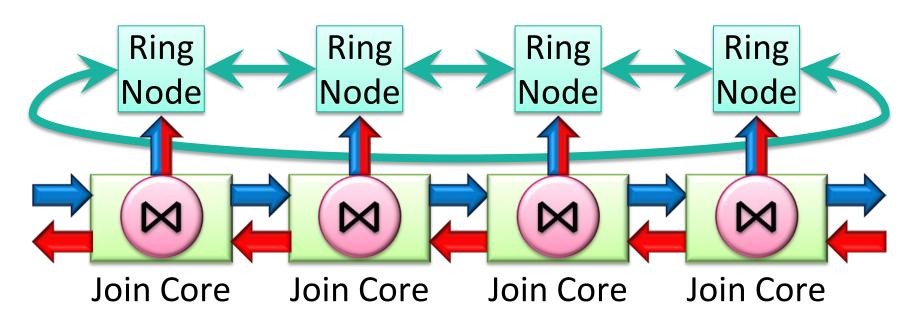




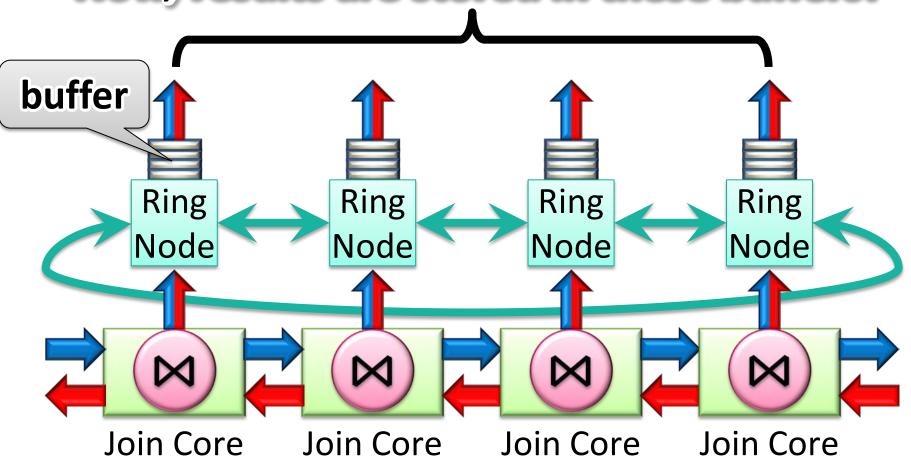
PROPOSED IMPLEMENTATION Adaptive Merging Network

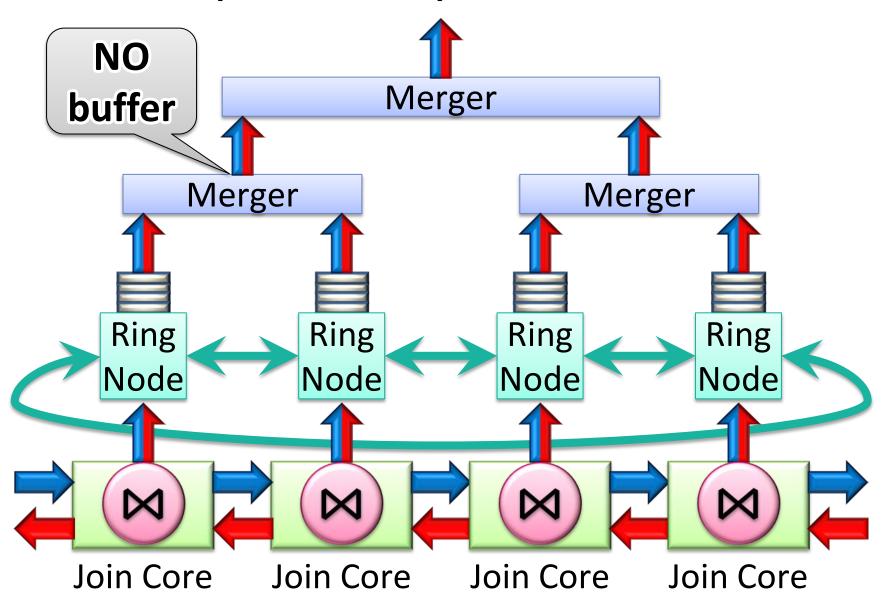




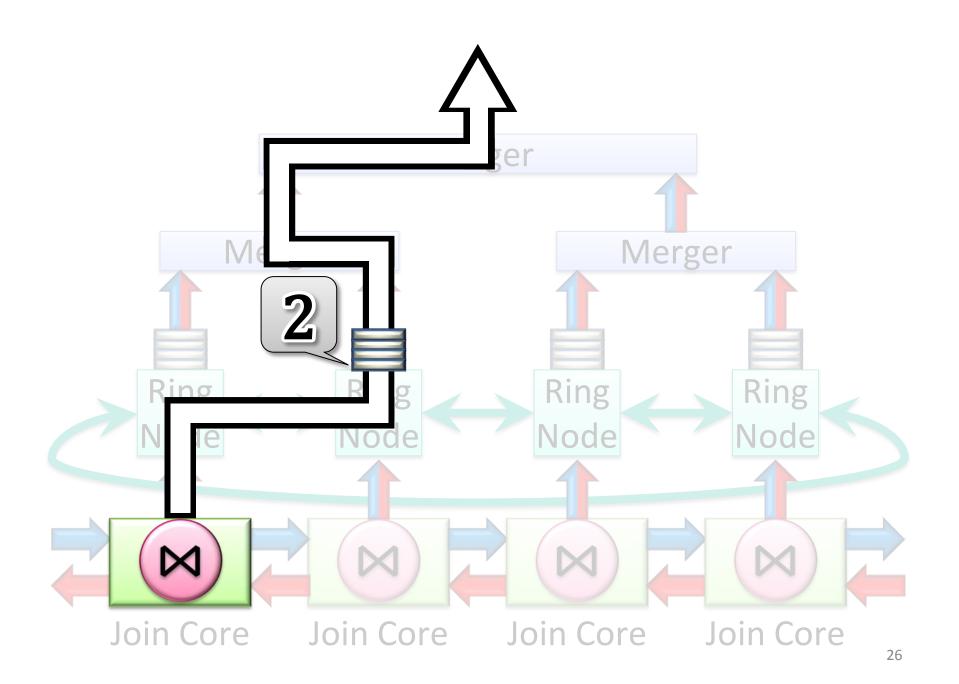


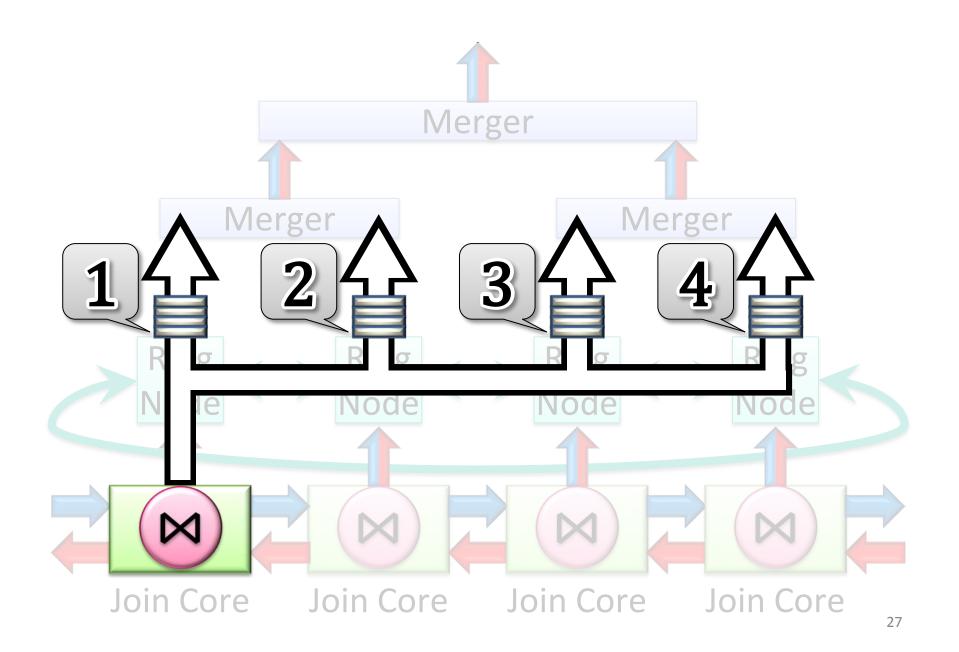
Now, results are stored in these buffers!

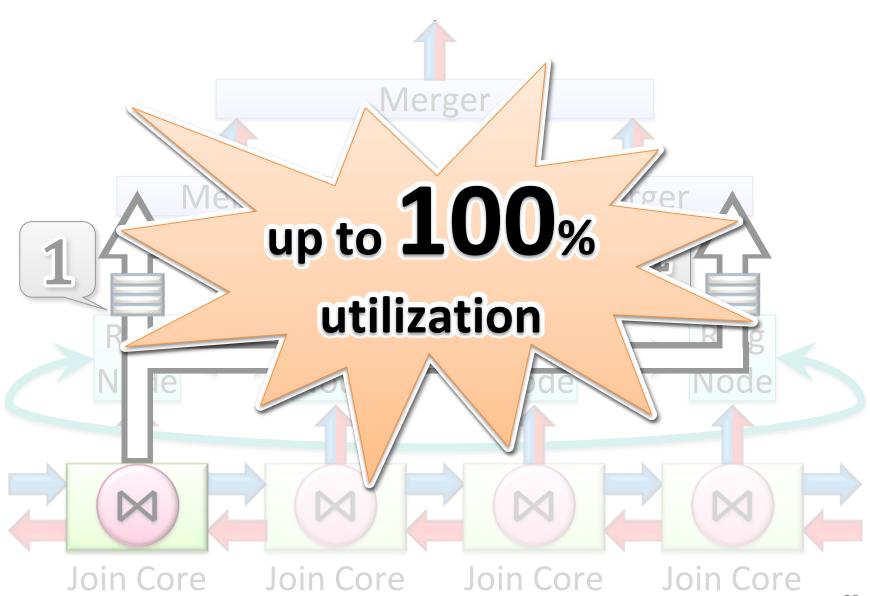




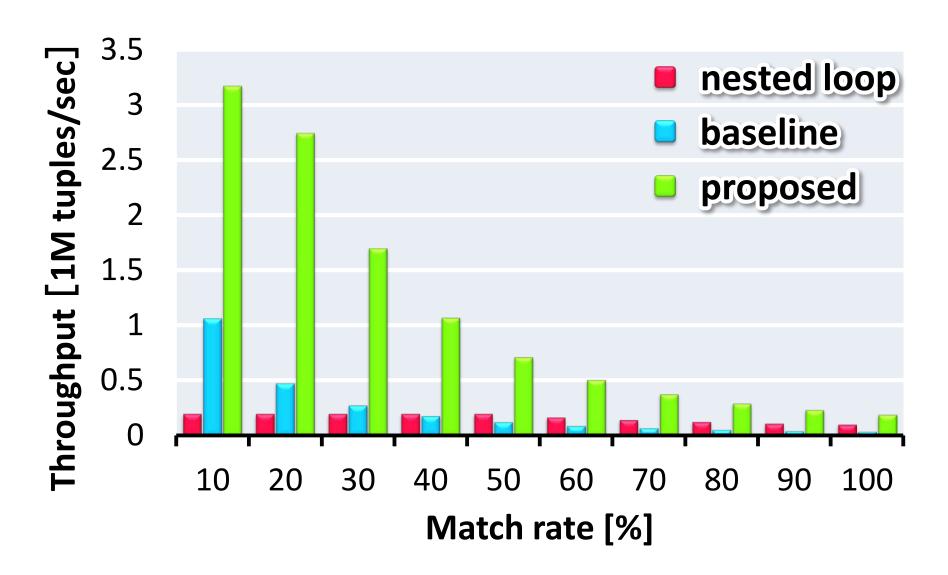
Output Data Flow Merger ger Ring Ring Ring Node Node Node Join Core Join Core Join Core Join Core 25



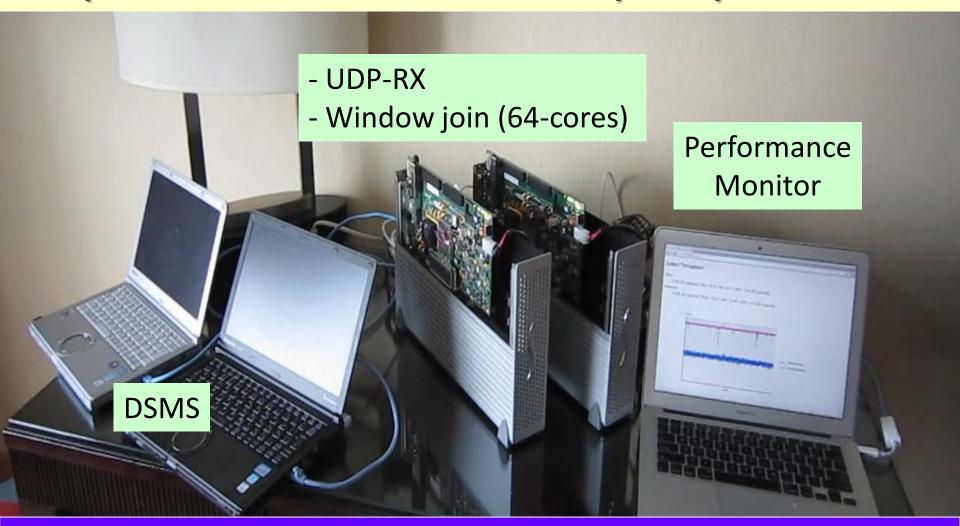




Performance | Proposed (64 join cores)



Basic: 6.7 millions of tuples per second Proposal: 14.6 millions of tuples per second



Demo @ SSDBM'13

Publications

- Yasin Oge, Takefumi Miyoshi, <u>Hideyuki Kawashima</u>, Tsutomu Yoshinaga: "A fast handshake join implementation on FPGA with adaptive merging network". <u>SSDBM</u> 2013
- Yasin OGE, Takefumi MIYOSHI, <u>Hideyuki KAWASHIMA</u>, Tsutomu YOSHINAGA: "Design and Implementation of a Handshake Join Architecture on FPGA", IEICE TRANSACTIONS(Journal) Vol.E95-D No.12 pp.2919-2927
- Yasin Oge, Takefumi MIYOSHI, <u>Hideyuki Kawashima</u>, Tsutomu Yoshinaga, "Design and Implementation of a Merging Network Architecture for Handshake Join Operator on FPGA", IEEE MCSoC, pp.84-91, Sep. 2012.
- Yasin Oge, Takefumi Miyoshi, <u>Hideyuki Kawashima</u>, and Tsutomu Yoshinaga, "An Implementation of Handshake Join on FPGA", ICNC, pp.95-104, Dec. 2011.
- Takefumi Miyoshi, <u>Hideyuki Kawashima</u>, Yuta Terada and Tsutomu Yoshinaga, "A Coarse Grain Reconfigurable Processor Architecture for Stream Processing Engine", FPL, Sep. 5-7, 2011

